003-003207

B.C.A. (CBCS) Sem.-II Examination

April-2014

CS-09 Computer Organization & Architecture

(New Course)

Faculty Code : 003

Subject Code : 003207

Time : 2 ½ Hours] [**Total Marks : 70**

Instructions :(1) Answer of Question – **1** MCQ must be written on Answer sheet. **All** questions are compulsory.

(2) Draw figure wherever necessary.

1. M.C.Q. **20**

   (1) An inverter is also called a _____ gate.
      (a) OR (b) NOT
      (c) AND (d) NAND

   (2) How many memory location can 14 address bits location have ?
      (a) 8, 192 (b) 16, 384
      (c) 14, 192 (d) 4, 096

   (3) A _____ is a group of devices that stores digital duty.
      (a) Register (b) Circuit
      (c) Bit (d) Byte

   (4) A shift Register can be used for
      (a) Serial to parallel conversion
      (b) Parallel to serial conversion
      (c) Digital delay lines
      (d) All of the above

   (5) A Flip Flop can store _____.
      (a) 2 Bit of duty (b) 3 Bit of duty
      (c) 1 Bit of duty (d) None of the above

   (6) Binary coded decimal number express each digital as _____
      (a) Nibble (b) Byte
      (c) Bit (d) All of the above

(7) Division of 100011 by 101 is _____

    (a)   100   (b)   111

    (c)   101   (d)   1010

(8) Conversion of Octal 35_8 to binary is _____

    (a)   011101   (b)   011100

    (c)   011110   (d)   011111

(9) The control and ALU sections are called _____

    (a)   Block Diagram   (b)   Control Unit

    (c)   Input/Output Unit   (d)   Central Processing Unit

(10) Software Interrupt is initiated by _____

    (a)   Signals   (b)   Waveform

    (c)   Executing an instruction (d)   None

(11) Stack means _____

    (a)   FIFO   (b)   LIFO

    (c)   LIIO   (d)   None

(12) $A + B * C$ Prefix of it is _____

    (a) $+ ABC *$ (b) $+ A * BC$

    (c) $ABC * +$ (d) $+ * ABC$

(13) Which of the following is not type of Interrupts ?

    (a)   External interrupt   (b)   Internal interrupt

    (c)   Software interrupt   (d)   Analog interrupt

(14) Parallel Address are _____

    (a)   Combinational Logic Units

    (b)   Sequential Logic Circuit

    (c)   Both of above

    (d)   None

(15) DMA stands for _____

    (a)   Direct Memory Alteration (b)   Direct Memory Access

    (c)   Direct Module   (d)   None

(16) Which Bus is bidirectional ?

    (a)   Address Bus   (b)   Data Bus

    (c)   Control Bus   (d)   None

(17) Linkage between CPU ? User is provided by _____.

    (a)   Storage   (b)   Control unit

    (c)   Peripheral devices   (d)   Software

(18) Which of the following is not an output devices of computer ?

    (a)   Printer   (b)   Keyboard

    (c)   VDC   (d)   Scanner